

We claim:

1. A method for identifying a correct command entry address, the method which comprises:

providing each one of a plurality of short command words with a first start bit having a predetermined value;

providing each one of a plurality of long command words with the first start bit having the predetermined value and with a second start bit having a predetermined value; and

outputting a signal from a checking apparatus if a command entry address is not correct.

2. The method according to claim 1, which further comprises: outputting a signal from the checking apparatus if an intended entry point is the second start bit.

3. The method according to claim 2, which further comprises:

providing each one of the plurality of long command words with a first command word half having a beginning and a second command word half having a beginning;

configuring the first start bit at the beginning of the first command word half; and

configuring the second start bit at the beginning of the second command word half.

4. The method according to claim 3, which further comprises: providing the value of the second start bit as an inverse of the value of the first start bit.

5. The method according to claim 2, which further comprises: providing the value of the second start bit as an inverse of the value of the first start bit.